



Customer No. 42717

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:		§	
	Jhon Jhy Liaw	§	Confirmation No.: 8019
		§	
Serial No.: 10/687,424		§	Group Art Unit: 1765
		§	
Filed:	October 16, 2003	§	Examiner: Kin Chan Chen
		§	
For:	Process Integration of SOI FETs with	§	
	Active Layer Spacer	§	

Mail Stop: AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO NON-FINAL OFFICE ACTION MAILED SEPTEMBER 6, 2005

The present paper is submitted in response to the Non-Final Office Action of September 6, 2005. No fees, including extension of time fees, are believed necessary for consideration of the present paper. However, if other fees are necessary, including extension of time fees, the extension of time is hereby requested, and the Commissioner is hereby authorized to charge any fees, including those for the extension of time, to Haynes and Boone, LLP's Deposit Account No. 08-1394.

Amendments to the Claims are reflected in the listing of claims beginning on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

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